

7 wherein each set of [color pixel sensors] color pixel
8 elements is configured to allow independent integration times.

1 2. (Amended) The integrated circuit of claim 1, further
2 comprising:

3 a set of reset shift registers coupled to the set of first
4 [color pixel sensors] color pixel elements, the set of second
5 [color pixel sensors] color pixel elements, and the set of third
6 [color pixel sensors] color pixel elements; and,

7 a control unit coupled to said set of reset shift registers;

8 wherein the control unit is configured for sequencing a set of
9 input bits for the set of reset shift registers according to a set
10 of desired integration times.

1 3. (Unchanged) The integrated circuit of claim 1, wherein each
2 row of pixel sensor in the set of pixel sensors has a wordline and
3 the integrated circuit further comprising a wordline shift
4 register coupled to each row of pixel sensors, the wordline shift
5 register having at least one output per row of pixel sensors
6 coupled to each wordline.

1 4. (Unchanged) The integrated circuit of claim 3, where the
2 control unit further sequences a set of output bits for the
3 wordline shift register.

1 5. (Amended) The integrated circuit of claim 2, where each set
2 of [color pixel sensors] color pixel elements is coupled to

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A2. 3 corresponding reset shift registers in the set of reset shift
4 registers.

1 6. (Unchanged) The integrated circuit of claim 2, where the set
2 of integration times includes a first color integration time, a
3 second color integration time, and a third color integration time.

A3 1 7. (Amended) A method comprising:
2 determining a lighting environment;
3 determining a set of predetermined integration times based on
4 the lighting environment; and,
5 controlling a set of pixel sensors based on the set of
6 predetermined integration times, where the set of pixel sensors
7 has a first set of [color pixel sensors] color pixel elements, a
8 second set of [color pixel sensors] color pixel elements, and a
9 third set of [color pixel sensors] color pixel elements and each
10 set of [color pixel sensors] color pixel elements has an
11 associated integration time in the set of predetermined
12 integration times.

1 8. (Unchanged) The method of claim 7, where controlling the set
2 of pixel sensors comprises generating a set of reset bits for
3 placement into a set of reset shift registers.

1 9. (Unchanged) The method of claim 8, where generating a set of
2 reset bits comprises:

3 generating a set of first color reset bits for placement into
4 a first color reset shift register based on the set of
5 predetermined integration times;

6 generating a set of second color reset bits for placement
7 into a second color reset shift register based on the set of
8 predetermined integration times; and,

9 generating a set of third color reset bits for placement into
10 a third color reset shift register based on the set of
11 predetermined integration times.

1 10. (Unchanged) The method of claim 7, further comprising
2 reading a set of pixel sensors based on the set of predetermined
3 integration times.

1 11. (Unchanged) The method of claim 10, where reading the set of
2 pixel sensors comprises generating a set of wordline bits for
3 placement into a wordline shift register.

1 12. (Amended) An apparatus comprising:

2 means for determining a lighting environment;

3 means for determining a set of predetermined integration
4 times based on the lighting environment; and,

5 means for controlling a set of pixel sensors based on the set
6 of predetermined integration times, where the set of pixel sensors
7 has a first set of [color pixel sensors] color pixel elements, a

8 second set of [color pixel sensors] color pixel elements, and a
9 third set of [color pixel sensors] color pixel elements and each
10 set of [color pixel sensors] color pixel elements has an
11 associated integration time in the set of predetermined
12 integration times.

1 13. (Unchanged) The apparatus of claim 12, where the means for
2 controlling the set of pixel sensors comprises a control unit
3 configured to generate a set of reset bits for placement into a
4 set of reset shift registers.

1 14. (Unchanged) The apparatus claim 13, where the control unit
2 comprises:

3 means for generating a set of first color reset bits for
4 placement into a first color reset shift register based on the set
5 of predetermined integration times;

6 means for generating a set of second color reset bits for
7 placement into a second color reset shift register based on the
8 set of predetermined integration times; and,

9 means for generating a set of third color reset bits for
10 placement into a third color reset shift register based on the set
11 of predetermined integration times.

1 15. (Unchanged) The apparatus of claim 12, further comprising
2 means for reading a set of pixel sensors based on the set of
3 predetermined integration times.

1 16. (Unchanged) The apparatus of claim 15, where the means for
2 reading the set of pixel sensors comprises a control unit
3 configured to generate a set of wordline bits for placement into a
4 wordline shift register.

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1 17. (Amended) An apparatus comprising:

2 a lens;

3 an integrated circuit coupled to the lens including:

4 a pixel sensor array having a set of pixel sensors
5 arranged in a set of rows and a set of columns, the set
6 of pixel sensors having a set of first [color pixel
7 sensors] color pixel elements, a set of second [color
8 pixel sensors] color pixel elements, and a set of third
9 [color pixel sensors] color pixel elements; wherein each
10 set of [color pixel sensors] color pixel elements is
11 configured to allow independent integration times;

12 a system controller coupled to the integrated circuit; and,

13 a local user interface unit coupled to the system controller.

1 18. (Amended) The apparatus of claim 17, further comprising:

2 a set of reset registers coupled to the set of first [color
3 pixel sensors] color pixel elements, the set of second [color
4 pixel sensors] color pixel elements, and the set of third [color
5 pixel sensors] color pixel elements; and,

6 a control unit coupled to said set of reset shift registers;

7 wherein the control unit is configured for sequencing a set

8 of input bits for the set of reset shift registers according to a

9 set of desired integration times.

1 19. (Unchanged) The apparatus of claim 17, wherein each row of

2 pixel sensor in the set of pixel sensors has a wordline and the

3 integrated circuit further comprising a wordline shift register

4 coupled to each row of pixel sensors, the wordline shift register

5 having at least one output per row of pixel sensors coupled to

6 each wordline.

1 20. (Unchanged) The integrated circuit of claim 19, where the

2 control unit further sequences a set of output bits for the

3 wordline shift register.

1 21. (Amended) The integrated circuit of claim 18, where each set

2 of [color pixel sensors] color pixel elements is coupled to

3 corresponding reset registers in the set of reset registers.

1 22. (Unchanged) The integrated circuit of claim 18, where the

2 set of integration times includes a first color integration time,

3 a second color integration time, and a third color integration

4 time.